

REMARKS

The Official Action mailed October 23, 2002 has been received and its contents carefully noted. Filed concurrently herewith is a *Request for One Month Extension of Time*, which extends the shortened statutory period for response to February 23, 2003. Accordingly, Applicant respectfully submits that this response is being timely filed.

Applicant notes with appreciation the consideration of the Information Disclosure Statements filed on March 24, 2000; November 30, 2001; and July 18, 2002. Applicant respectfully requests consideration of the Information Disclosure Statements filed December 19, 2000 and September 5, 2001. A further IDS is submitted herewith and careful review and consideration of this IDS is requested.

The specification has been amended to correct typographical errors.

Claims 2-43 are pending in the present application, of which claims 2, 7, 15, 21, 29 and 35 are independent. For the reasons set forth in detail below, all claims are believed to be in condition for allowance.

The Official Action rejects claims 2 and 4 as being unpatentable over U.S. Patent No. 5,210,050 to Yamazaki et al., U.S. Patent No. 4,140,548 to Zimmer and U.S. Patent No. 5,488,000 to Zhang et al. The Official Action further rejects claims 3-43 as being unpatentable over Yamazaki '050, Zimmer, Zhang and one or more of the following: U.S. Patent No. 5,372,860 to Fehlner et al., U.S. Patent No. 5,365,080 to Yamazaki et al., U.S. Patent No. 5,313,076 to Yamazaki et al., and U.S. Patent 5,913,111 to Kataoka et al. For example, claims 8-9 and 12-43 are rejected as being unpatentable over Yamazaki '050, Zimmer, Zhang, Fehlner, Yamazaki '080, Yamazaki '076 and Kataoka. The Applicants respectfully traverse the rejection because the Official Action has not made a *prima facie* case of obviousness.

As stated in MPEP §§ 2143-2143.01, to establish a *prima facie* case of obviousness, three basic criteria must be met. First, there must be some suggestion or motivation, either in the references themselves or in the knowledge generally available to one of ordinary skill in the art, to modify the reference or to combine reference teachings. Second, there must be a reasonable expectation of success. Finally, the prior art reference (or references when combined) must teach or suggest all the claim limitations. Obviousness can only be established by combining or modifying the teachings of the prior

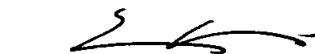
art to produce the claimed invention where there is some teaching, suggestion, or motivation to do so found either explicitly or implicitly in the references themselves or in the knowledge generally available to one of ordinary skill in the art. "The test for an implicit showing is what the combined teachings, knowledge of one of ordinary skill in the art, and the nature of the problem to be solved as a whole would have suggested to those of ordinary skill in the art." *In re Kotzab*, 217 F.3d 1365, 1370, 55 USPQ2d 1313, 1317 (Fed. Cir. 2000). See also *In re Fine*, 837 F.2d 1071, 5 USPQ2d 1596 (Fed. Cir. 1988); *In re Jones*, 958 F.2d 347, 21 USPQ2d 1941 (Fed. Cir. 1992).

The prior art, either alone or in combination, does not teach or suggest all the features of the independent claims. Specifically, the prior art does not teach or suggest an EL display device having the feature recited in each of independent claims 2, 7, 15, 21, 29 and 35. That is, for example, an EL display device having at least one thin film transistor comprising a first insulating film comprising a thermal oxide film of a semiconductor film thereon, a second insulating film comprising a deposition film over the semiconductor film, and the semiconductor film containing hydrogen atoms at a concentration of $1 \times 10^{17} \text{ cm}^{-3}$ to $1 \times 10^{21} \text{ cm}^{-3}$ and oxygen atoms at a concentration of $2 \times 10^{19} \text{ cm}^{-3}$ or less (claim 2) is not disclosed or suggested either explicitly or inherently.

Since the prior art references, alone or in combination, do not teach or suggest all the claim limitations, a *prima facie* case of obviousness cannot be maintained. Accordingly, reconsideration and withdrawal of the rejection of claims 2-43 under 35 U.S.C. § 103(a) is in order and respectfully requested.

Should the Examiner believe that anything further would be desirable to place this application in better condition for allowance, the Examiner is invited to contact Applicant's undersigned attorney at the telephone number listed below.

Respectfully submitted,



Eric J. Robinson
Reg. No. 38,285

Robinson Intellectual Property Law Office, P.C.
PMB 955
21010 Southbank Street
Potomac Falls, Virginia 20165
(571) 434-6789

VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE SPECIFICATION:

Please amend the specification as follows:

Please replace the first and second paragraphs on page 6 with the following:

A first embodiment of the present invention is characterized in that it comprises steps of forming an amorphous silicon film; holding a metal element which promotes crystallization of silicon in contact on the surface of the amorphous silicon film; crystallizing the amorphous silicon film by implementing a first heat treatment to obtain a crystal silicon film; forming a thermal oxide film on the surface of the crystal silicon film by implementing a second heat treatment in the temperature range of 500[.]° C to 700[.]° C within an atmosphere containing oxygen, hydrogen and fluorine; and eliminating the thermal oxide film.

A second embodiment of the present invention is characterized in that it comprises steps of forming an amorphous silicon film; holding a metal element which promotes crystallization of silicon in contact on the surface of the amorphous silicon film; crystallizing the amorphous silicon film by implementing a first heat treatment to obtain a crystal silicon film; forming a thermal oxide film on the surface of the crystal silicon film by implementing a second heat treatment in the temperature range of 500[.]° C to 700[.]° C within an atmosphere containing oxygen, hydrogen, fluorine and chlorine; and eliminating the thermal oxide film.

Please replace the paragraph bridging pages 8 and 9 with the following:

According to a preferred mode for carrying out the invention disclosed in the present specification, an amorphous silicon film is formed on a glass substrate at first. Then, the amorphous silicon film is crystallized by an action of metal element typified by nickel which promotes crystallization of silicon to obtain a crystal silicon film. [[0041]] This crystallization

is carried out by a heat treatment in a temperature of 500[.]° C to 700[.]° C. The metal element is contained in the crystal silicon film in the state in which the film has been crystallized by the above-mentioned heat treatment.

Please replace the third paragraph on page 10 with the following:

A fabrication process of the present embodiment will be explained by using FIG. 1 below. At first, a silicon oxide film 102 is formed as an underlying film in a thickness of 3000 angstrom on the glass substrate 101 of [Coning] Corning 1737 (distortion point: 667[.]° C).

Please replace the fifth paragraph on page 13 with the following:

Next, a heat treatment is implemented in the temperature range from 500[.]° C to 700[.]° C in the state shown in FIG. 1B to crystallize the amorphous silicon film 103 and to obtain a crystal silicon film 105. This heat treatment is implemented in a nitrogen atmosphere containing 3% of hydrogen at 640[.]° C.

Please replace the paragraph bridging pages 13 and 14 with the following:

It is preferable to implement the heat treatment below the temperature of the distortion point of the glass substrate. Because the distortion point of the [Coning] Corning 1737 glass substrate is 667[.]° C, the upper limit of the heating temperature here is preferable to be about 650[.]° C, leaving some margin.

Please replace the third paragraph on page 15 with the following:

Here, this heat treatment is implemented within an oxygen atmosphere containing 3% of hydrogen at 100 ppm of CLF3 at 640[.]° C. In this step, the thermal oxide film is formed in a thickness of 200 angstrom (FIG. 1D).

Please replace the fifth paragraph on page 15 with the following:

This heat treatment is implemented at a temperature range from 500 to 700[.]° C. The upper limit of the heating temperature is limited by the distortion point of the glass substrate used. [It] One must be careful not to implement the heat treatment above the distortion point of the glass substrate because, otherwise, the substrate deforms.

Please replace the last paragraph on page 17 with the following:

The present embodiment relates to a case of growing crystal in the form different from that in the first embodiment. That is, the present embodiment relates to a method of growing the crystal in a direction parallel to the substrate, i.e. a method called lateral growth, by utilizing the metal element which promotes crystallization of silicon. [[0096]]

Please replace the first paragraph on page 18 with the following:

FIG. 2 shows the fabrication process according to the present embodiment. At first, a silicon oxide film is formed as an underlying film 202 in a thickness of 3000 angstrom on the [Coning] Corning 1737 glass substrate (or a quartz substrate) 201.

Please replace the fourth paragraph on page 18 with the following:

The opening 205 has a thin and long rectangular shape in the longitudinal direction from the depth to the front side of the figure. Preferably, the width of the opening 203 is 20 [オ]m] μ m or more. The length thereof in the longitudinal direction may be determined arbitrarily.

Please replace the paragraph bridging pages 18 and 19 with the following:

Next, a heat treatment is implemented at 640[.]° C for four hours in a nitrogen atmosphere containing 3% of hydrogen and in which oxygen is minimized. Then, crystal

grows in the direction parallel to the substrate as indicated by the reference numeral 207 in FIG. 2B. This crystal growth advances from the domain of the opening 205 to which nickel element has been introduced to the surrounding part. This crystal growth in the direction parallel to the substrate will be referred to as lateral growth.

Please replace the first full paragraph on page 19 with the following:

It is possible to advance this lateral growth across more than 100 [μ m] μ m under the conditions shown in the present embodiment. Then, a silicon film 208 having the domain in which the crystal has thus grown laterally is obtained. It is noted that crystal growth in the vertical direction called vertical growth advances from the surface of the silicon film to the underlying interface in the domain where the opening 205 is formed.

Please replace the first paragraph on page 20 with the following:

After irradiating the laser light, a heat treatment is implemented at 650[.]° C for 12 hours within an atmosphere [of] containing 3% of hydrogen and 100 ppm of NF_3 . In this step, an oxide film 209 containing nickel element in high concentration is formed in a thickness of 200 angstrom. In the same time, the concentration of nickel element within the silicon film 208 may be reduced relatively (FIG. 2D).

Please replace the second paragraph on page 21 with the following:

Then, a thermal oxide film 211 is formed after thus forming the pattern 210. This thermal oxide film is formed into a thickness of 200 angstrom by implementing a heat treatment for 12 hours in an oxygen atmosphere at 650[.]° C.

Please replace the paragraph bridging pages 22 and 23 with the following:

After removing the organic substances, thermal oxidation is implemented within an oxygen atmosphere at 640[.]° C to form a thermal oxide film 300 of 100 angstrom thick.

This thermal oxide film has a high interfacial characteristic with a semiconductor layer and composes a part of a gate insulating film later. Thus, the state shown in FIG. 3A is obtained.

Please replace the last paragraph on page 24 with the following:

The anodic oxide film may be grown up to several [μm] μm thick. The thickness is 6000 angstrom here. It is noted that the range of the growth may be controlled by adjusting an anodizing time.

On page 27, immediately after the sixth paragraph, please delete [0159].

Please replace the first paragraph on page 30 with the following:

Then, contact holes are created to form a source electrode 416 and a drain electrode 417. Finally, a heat treatment of an hour is implemented within a hydrogen atmosphere at 350[.]° C (hydrogen heat treatment). In this step, the defects and unpaired coupling hands within the active layer [is] are neutralized. Thus, the thin film transistor shown in FIG. 4E is completed.

Please replace the third paragraph on page 33 with the following:

Further, an interlayer insulating film 524 made of a resin material is formed by means of spin coating. Here, the thickness of the interlayer insulating film 524 is 1 [μm] μm (FIG. 5E).

Please replace the fourth and fifth paragraphs on page 35 with the following:

Next, a heat treatment is implemented within a nitrogen atmosphere containing 3% of hydrogen at 650[.]° C to crystallize the amorphous silicon film 605 and to obtain a crystal silicon film 606.

After forming the amorphous silicon film, a heat treatment is implemented within an oxygen atmosphere containing 5% of HCl and 100 ppm (volume) of NF_3 at 650[.]° C. A thermal oxide film 609 is formed in this heat treatment (FIG. 6C).